# CMOS基础

A：a；B：a；C：b；D：a

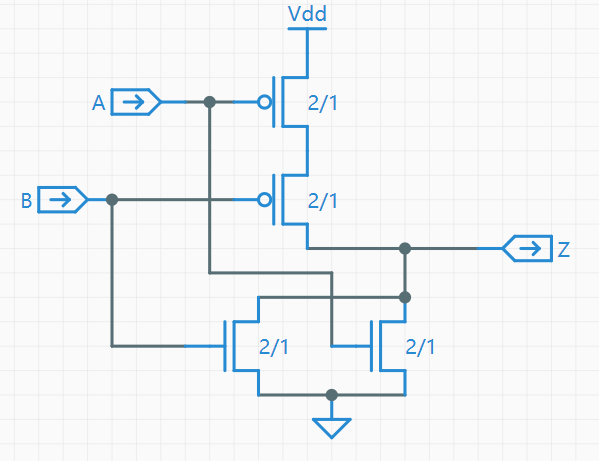
# 能量消耗

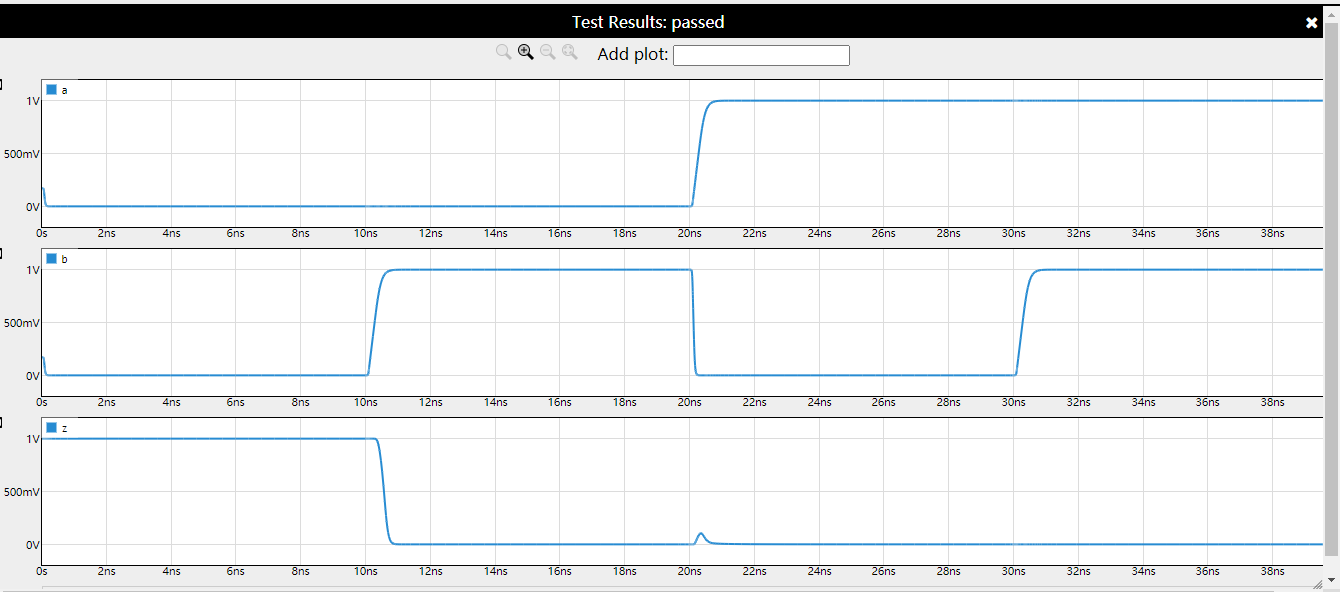
52/3.32 = 2.30

# CMOS逻辑门

A：4

NOR

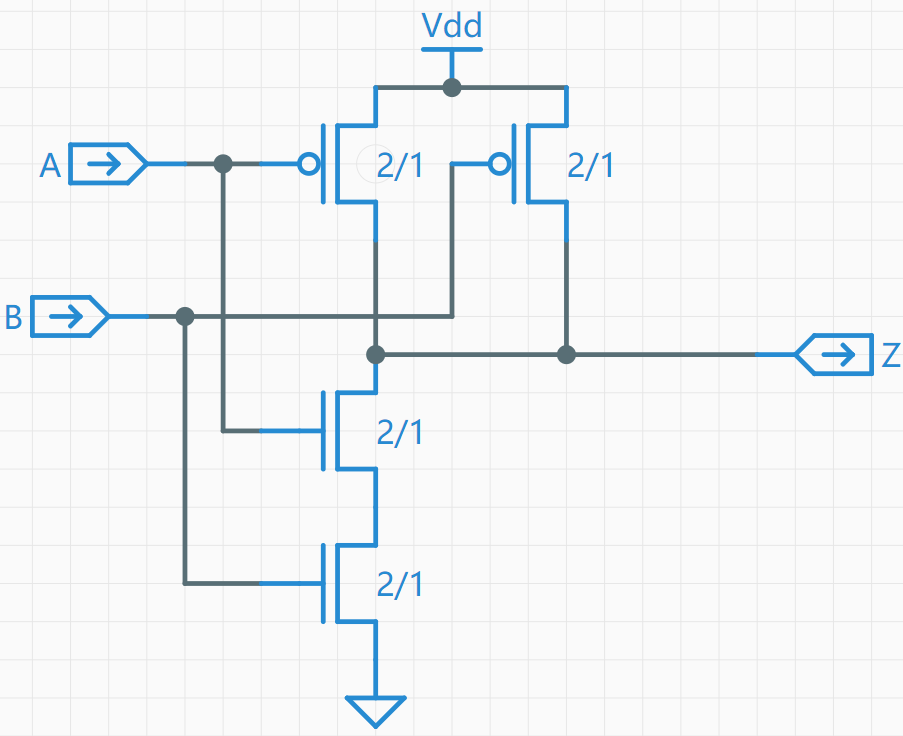


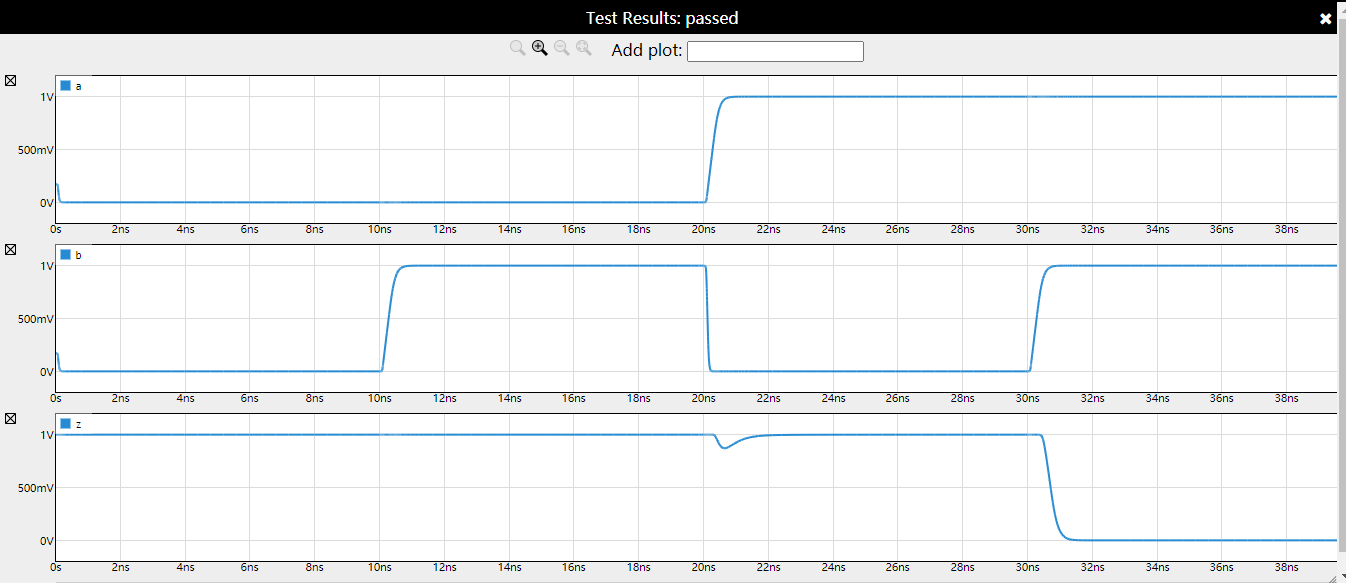


B：6

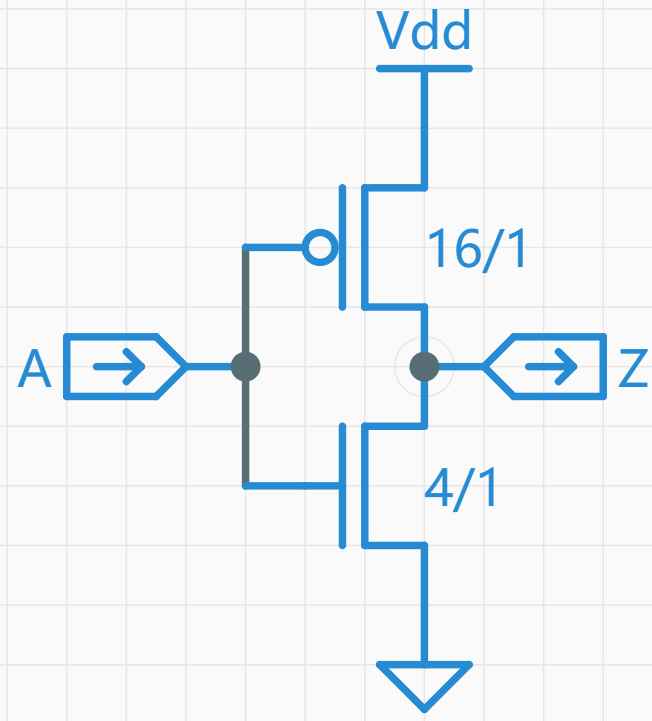
AND=NAND+NOT

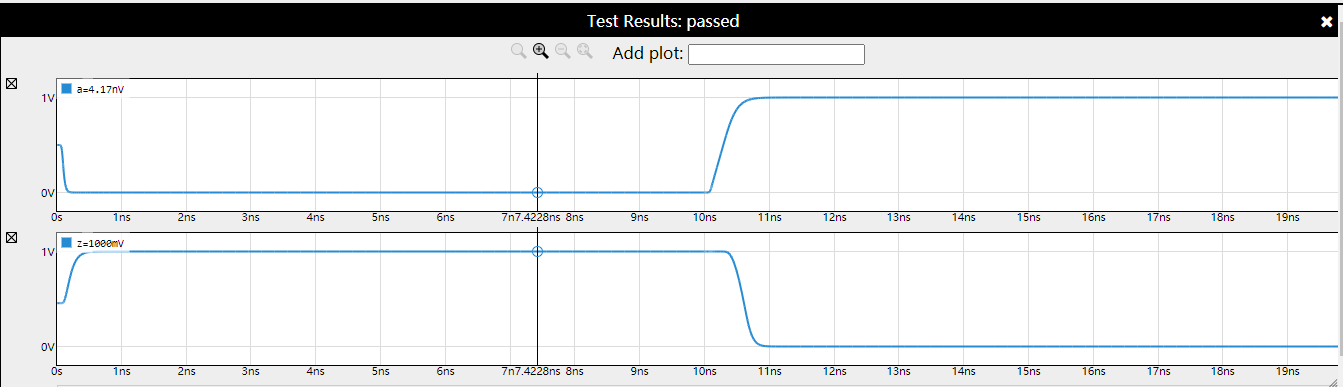
NAND





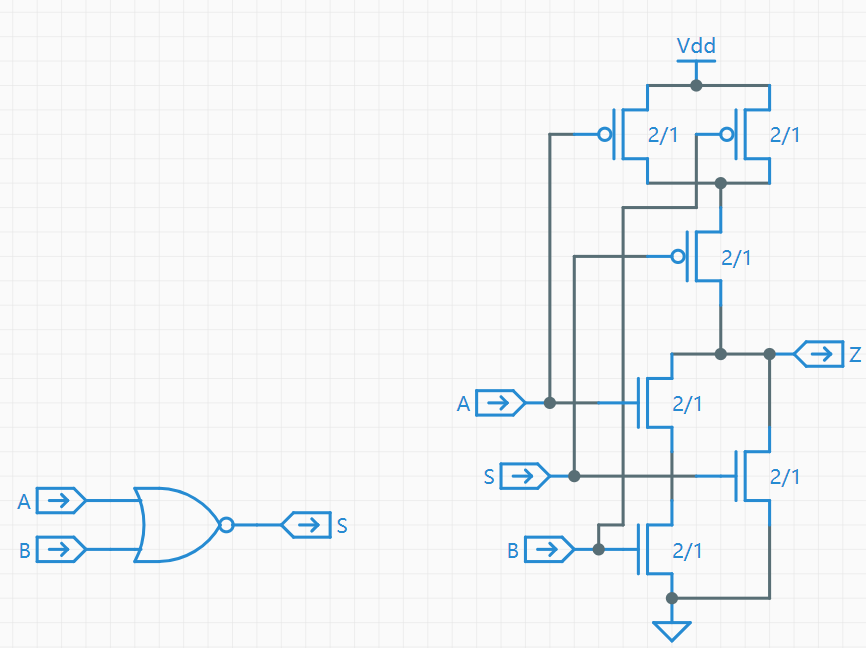
NOT

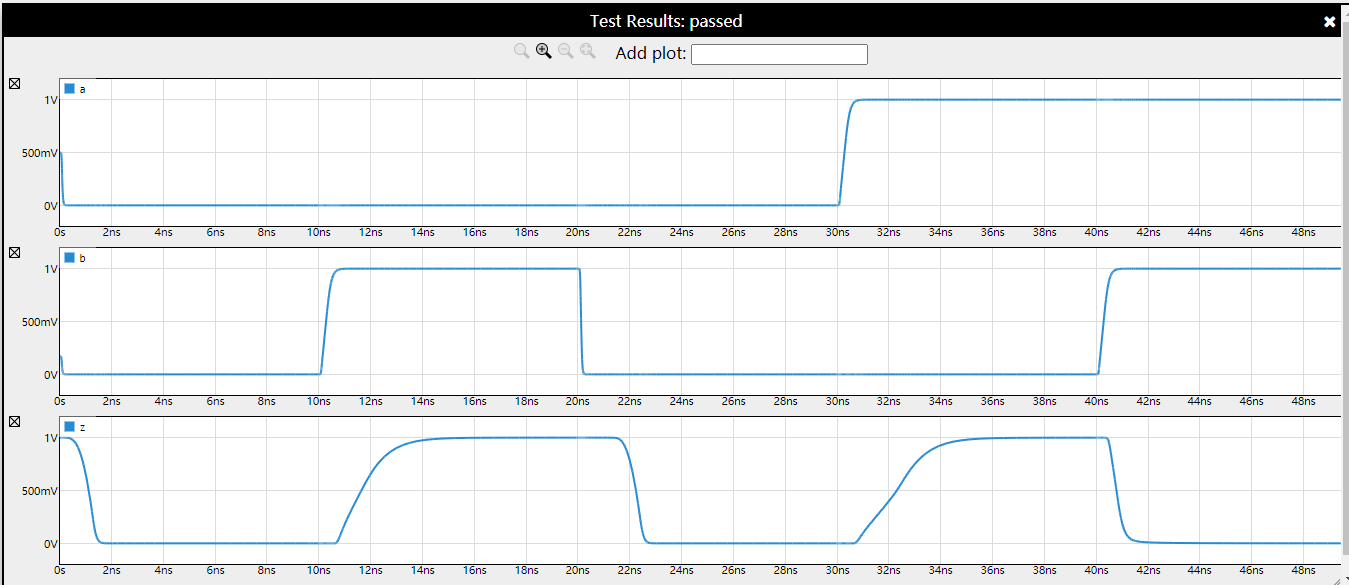




C：10

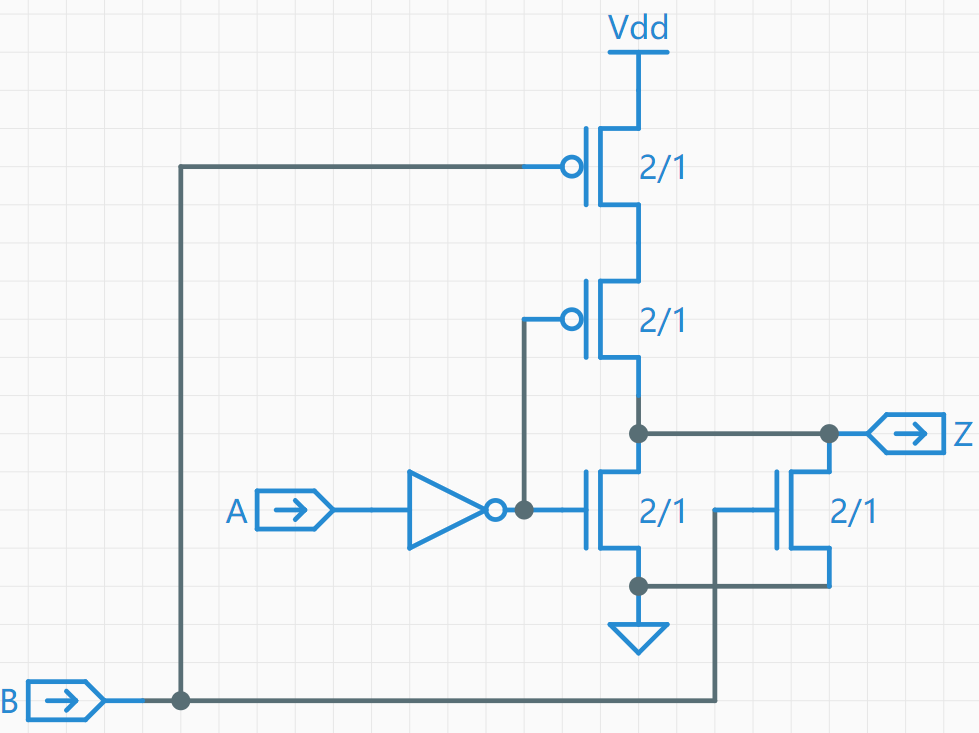
XOR

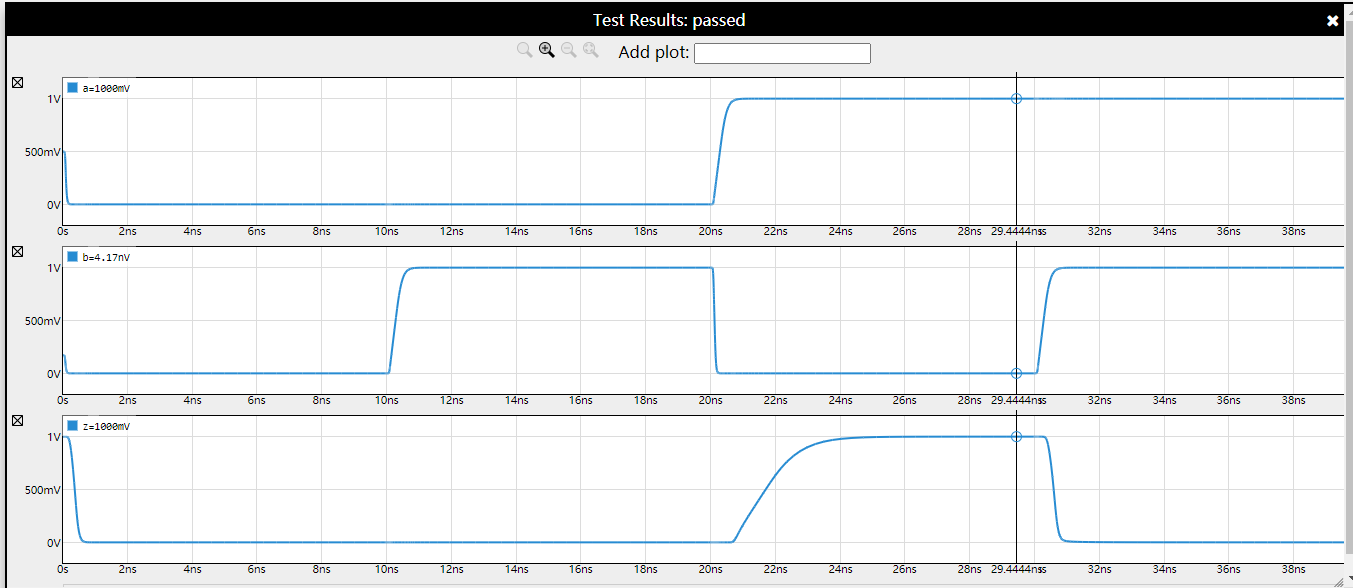




D：2

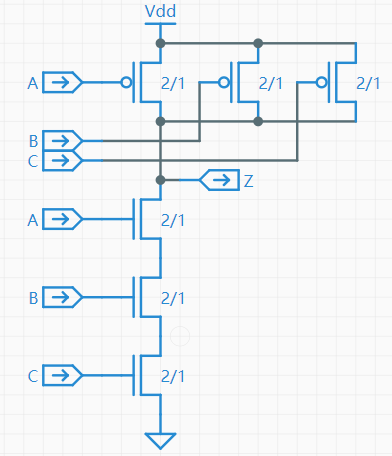
E：6

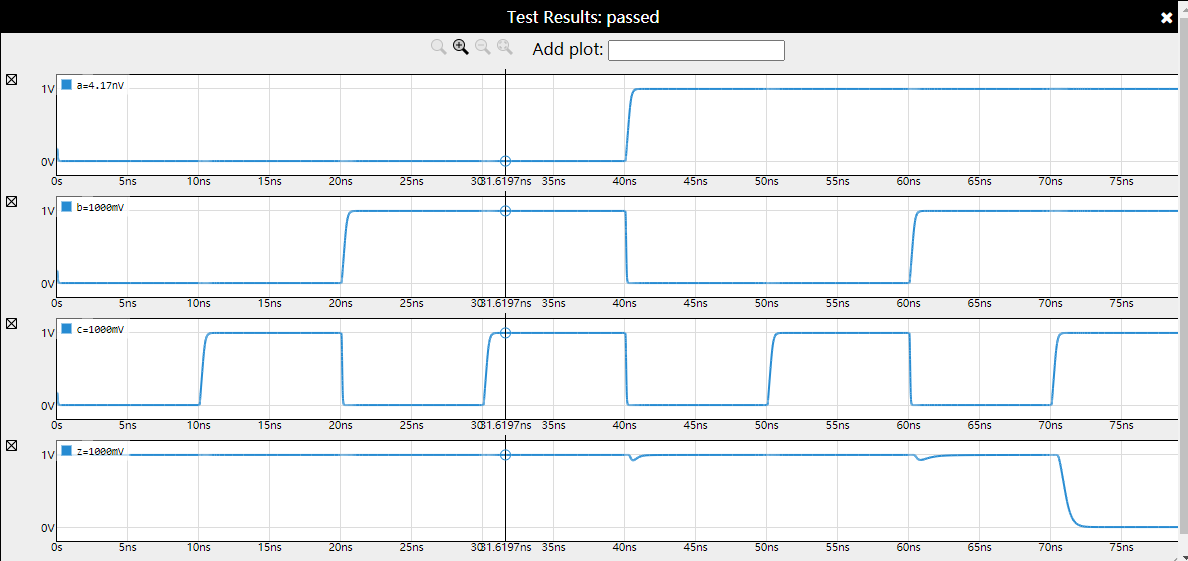




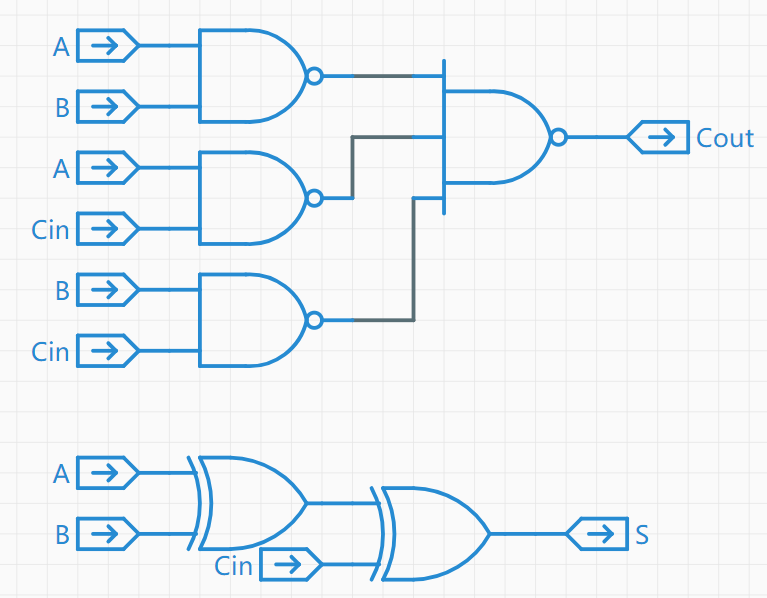
# 三位加法器

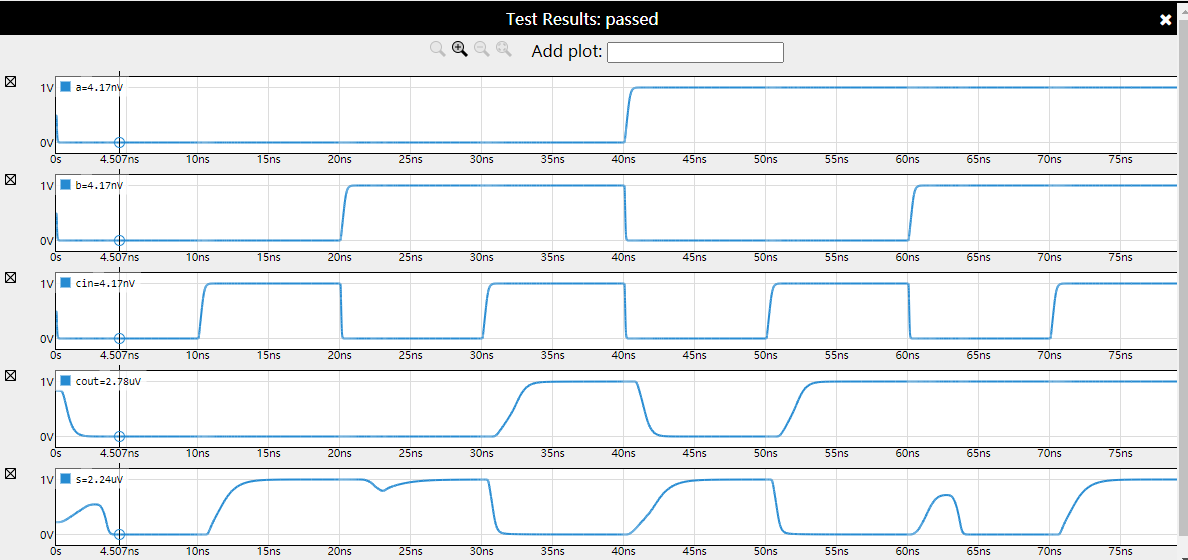
NAND3





fa





adder3

